

Electronic Acknowledgement Receipt

EFS ID:	4087436
Application Number:	09987978
International Application Number:	
Confirmation Number:	7905
Title of Invention:	LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME, AND METHOD OF AND PROCESS FOR FABRICATING THE TWO
First Named Inventor/Applicant Name:	Yujiro Kajihara
Customer Number:	20457
Filer:	Paul J. Skwierawski/Stacey Keaton
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Information Disclosure Statement Letter	32049RV1IDS.pdf	59966 9e1ed76629311ab94ee184826b67156f1ee1441	no	2
Warnings:					
Information:					
2	Information Disclosure Statement (IDS) Filed (SB/08)	32049RV1SB08.pdf	130221 e59b695a9eb2232c6182ccda57258d089792d4	no	1
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
3	Information Disclosure Statement Letter	32049OA.pdf	333994 2f1849138edf579120d8a4f959c7275d8d8091116	no	5
Warnings:					
Information:					
4	Foreign Reference	32049REF.pdf	268195 743c56618f71aa3bf12f53250e0cd273b96e2d8	no	8
Warnings:					
Information:					
5	Fee Worksheet (PTO-06)	fee-info.pdf	31008 ee31810361191b6b6f9ace78842bb10f5b0fd55d4	no	2
Warnings:					
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Total Files Size (in bytes):			823384		
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